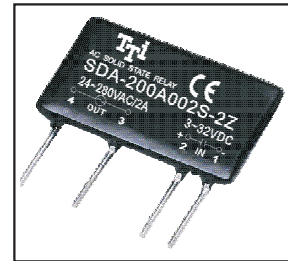


SDA SERIES SOLID STATE RELAY

SDA-200A002S-2Z
SDA-200A003S-2Z
SDA-200A004S-2Z
SDA-200A005S-2Z
SDA-200A006S-2Z

DC Control AC Loading S.S.R



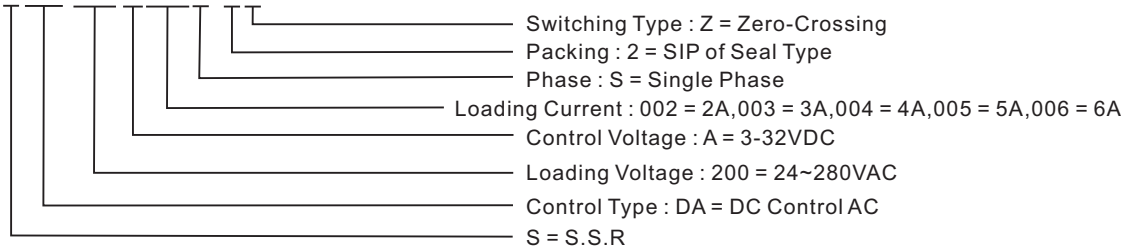
Specifications

MODEL SERIES NO.	CONTROL VOLTAGE	MUST TURN OFF VOLTAGE	INPUT IMPEDANCE	LOADING CURRENT	LOADING VOLTAGE	MIN BLOCKING VOLTAGE	MAX OFF-STATE LEAKAGE	FREQUENCY RANGE	MAX 1-CYCLE PEAK SURGE
SDA-200A002S-2Z	3 - 32 VDC	MAX 1.0 VDC	1.5 KΩ	2A	24 ~ 280VAC	600VAC	LESS 3 mA	47-70HZ	20A
SDA-200A003S-2Z	3 - 32 VDC	MAX 1.0 VDC	1.5 KΩ	3A	24 ~ 280VAC	600VAC	LESS 3 mA	47-70HZ	30A
SDA-200A004S-2Z	3 - 32 VDC	MAX 1.0 VDC	1.5 KΩ	4A	24 ~ 280VAC	600VAC	LESS 3 mA	47-70HZ	40A
SDA-200A005S-2Z	3 - 32 VDC	MAX 1.0 VDC	1.5 KΩ	5A	24 ~ 280VAC	600VAC	LESS 3 mA	47-70HZ	50A
SDA-200A006S-2Z	3 - 32 VDC	MAX 1.0 VDC	1.5 KΩ	6A	24 ~ 280VAC	600VAC	LESS 3 mA	47-70HZ	60A

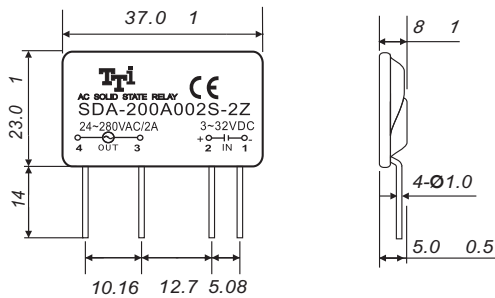
MODEL SERIES NO.	MAX OFF STATE dv/dt	MAX ON-STATE VOLTAGE DROP	ISOLATE IMPEDENCE	DIELECTRIC STRENGTH INPUT-OUTPUT	DIELECTRIC STRENGTH INPUT,OUTPUT-CASE	TURN ON TIME	TURN OFF TIME	CAPACITANCE IN-OUT	WEIGHT (g)
SDA-200A002S-2Z	200 V/μ sec	2.0Vrms	10 ⁹ Ω	2500 VACrms	—	LESS 2 msec	LESS 1/2 AC CYCLE	LESS 15 PF	12 g
SDA-200A003S-2Z	200 V/μ sec	2.0Vrms	10 ⁹ Ω	2500 VACrms	—	LESS 2 msec	LESS 1/2 AC CYCLE	LESS 15 PF	12 g
SDA-200A004S-2Z	200 V/μ sec	2.0Vrms	10 ⁹ Ω	2500 VACrms	—	LESS 2 msec	LESS 1/2 AC CYCLE	LESS 15 PF	12 g
SDA-200A005S-2Z	200 V/μ sec	2.0Vrms	10 ⁹ Ω	2500 VACrms	—	LESS 2 msec	LESS 1/2 AC CYCLE	LESS 15 PF	12 g
SDA-200A006S-2Z	200 V/μ sec	2.0Vrms	10 ⁹ Ω	2500 VACrms	—	LESS 2 msec	LESS 1/2 AC CYCLE	LESS 15 PF	12 g

Parts No.

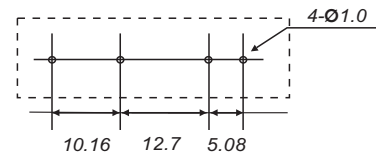
SDA-200A002S-2Z



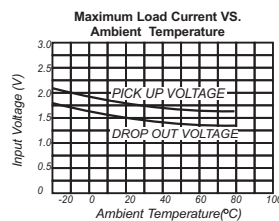
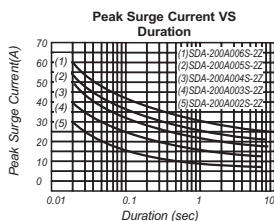
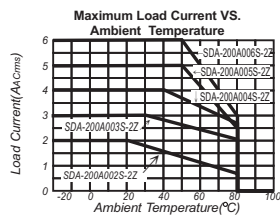
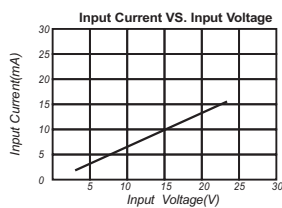
Outline Dimensions(Unit : mm)



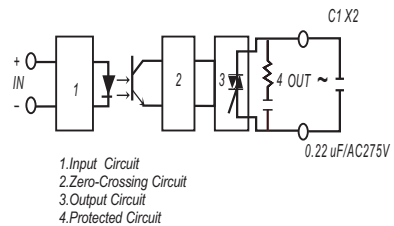
SIP of Seal Series



Characteristic Curves



Equivalent Circuit



Attention:

In order to be in compliance with the EMC Directive an additional X2 capacitor at the output is required if the SSR is operated as single component. In case the SSR is incorporated in an existing EMI filter may provide the required EMI suppression. The X2 capacitor must be placed as close as possible to the output terminals. See also above.